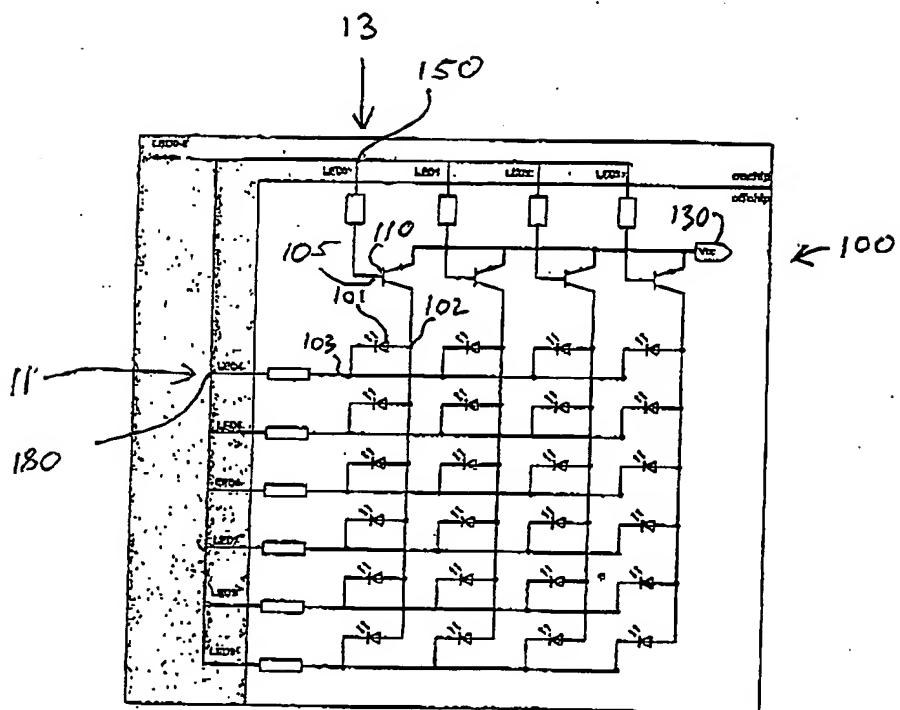


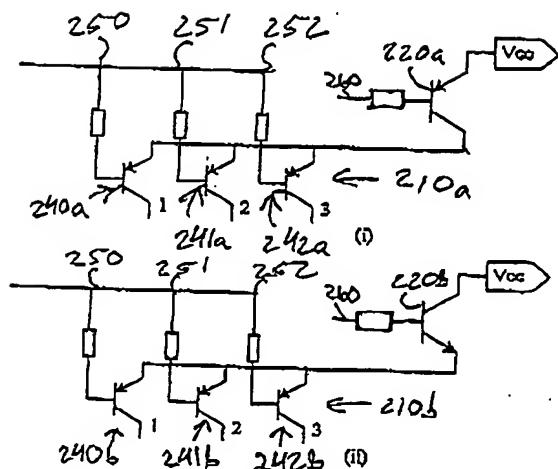
1 / 6



LED matrix as implemented in INCA-IP

Fig. 1

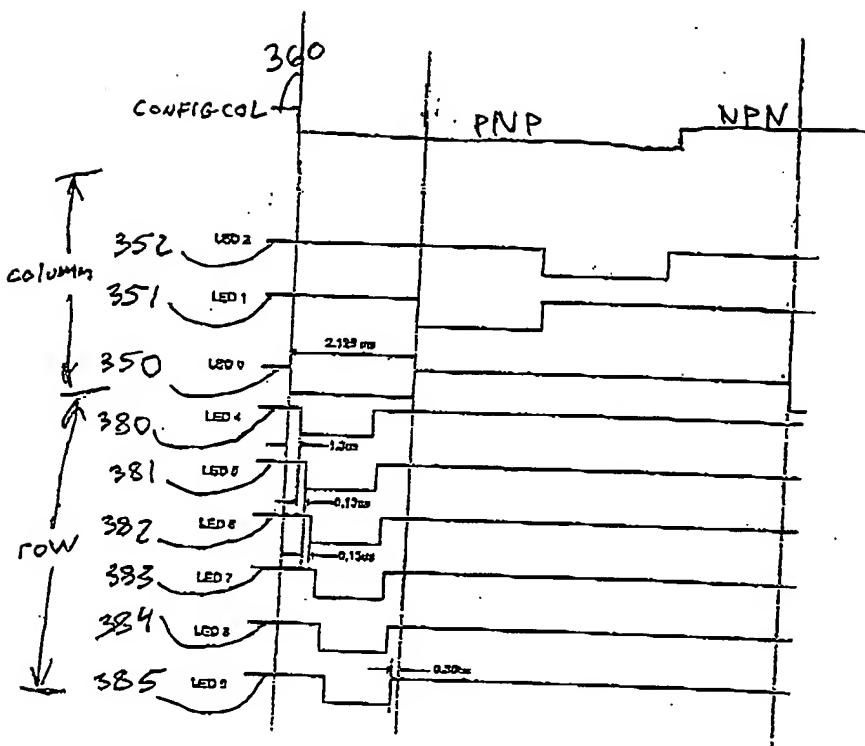
2 / 6



Row Decoding for output lines 1-4

Fig. 2

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Timing Diagram of signals on LED output lines.

Fig. 3

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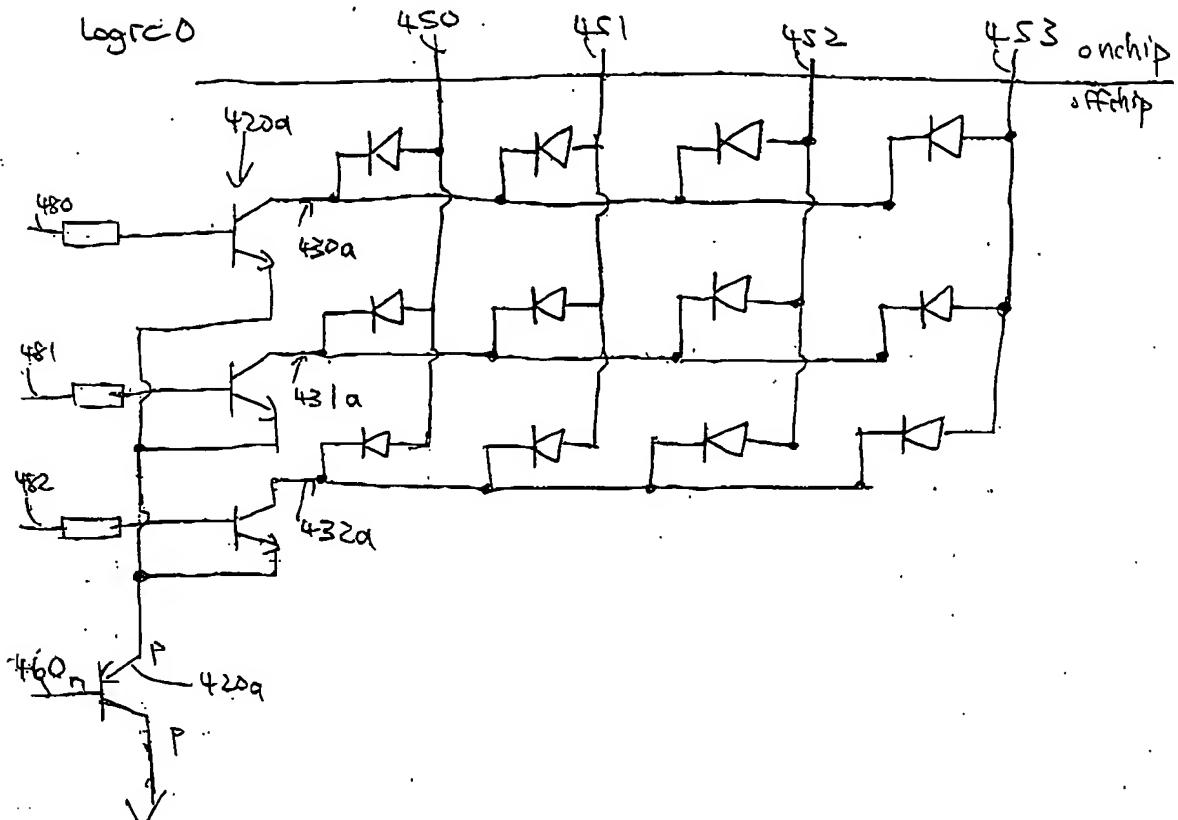


Fig. 4 (i)

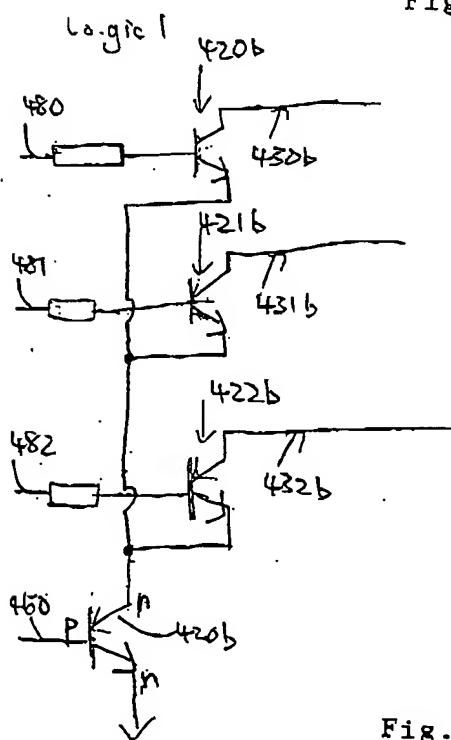
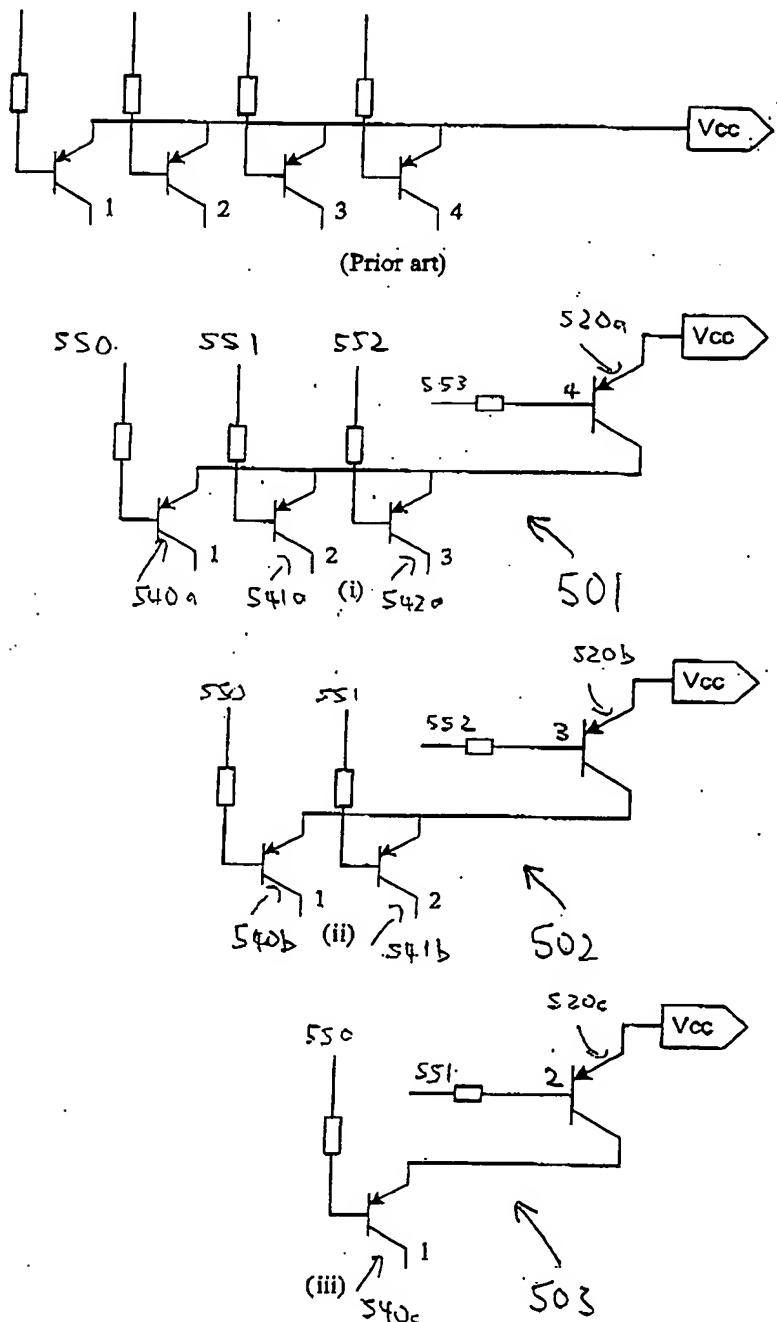


Fig. 4. (ii)

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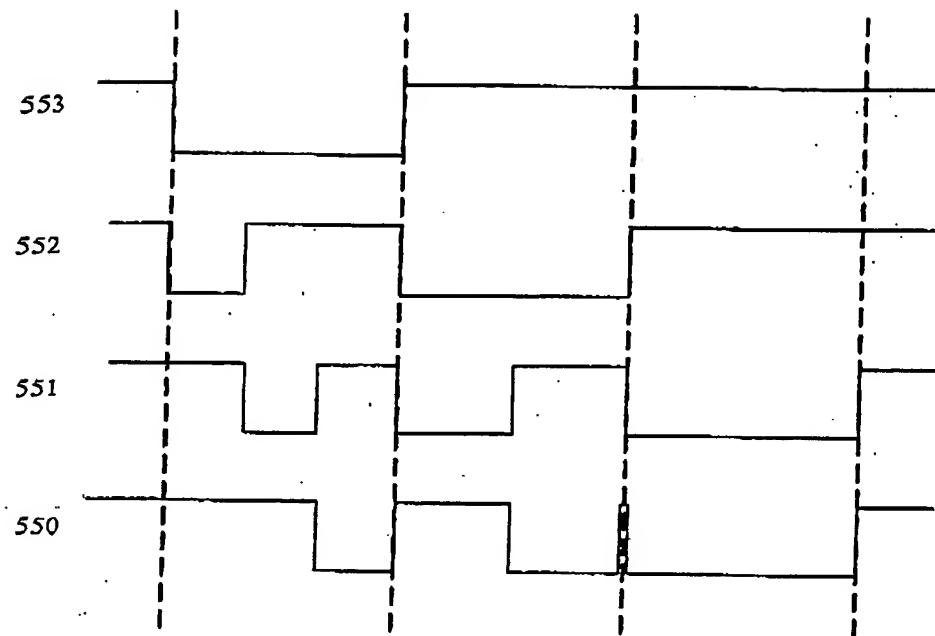
Full-Decoding



Decoding technique using all the column control input.

Fig. 5

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Timing Diagram with full-decoding

Fig. 6